

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Applicant(s): M. Horiguchi et al
Serial No.: 10/813,240
Filed: March 31, 2004
For: A SEMICONDUCTOR DEVICE HAVING REDUNDANCY CIRCUIT
Group: 2824
Examiner: J. HUR

AMENDMENT AND SUBMISSION OF TERMINAL DISCLAIMER

Commissioner For Patents
POB 1450
Alexandria, VA 22313-1450

December 27, 2004

Sir:

In response to the outstanding first Office Action, dated July 27, 2004, the period of response for which extension of time is requested in the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages. (A properly executed Terminal Disclaimer and an authorized credit card payment form covering the fee amount thereof are enclosed herewith.)

Amendments to the Specification;

Amendments to the Claims; and

Remarks are included following the Amendments.